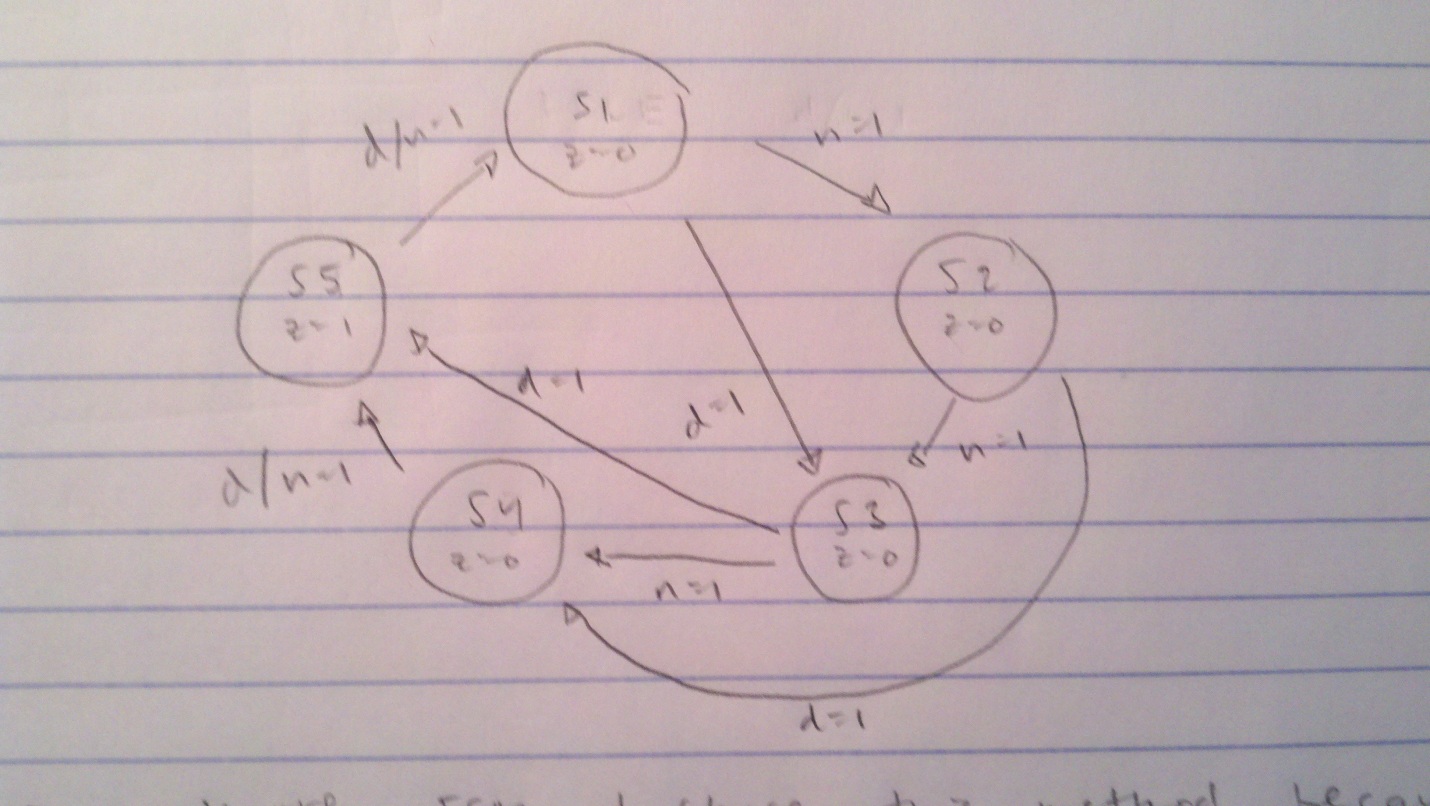
Erich Viebrock

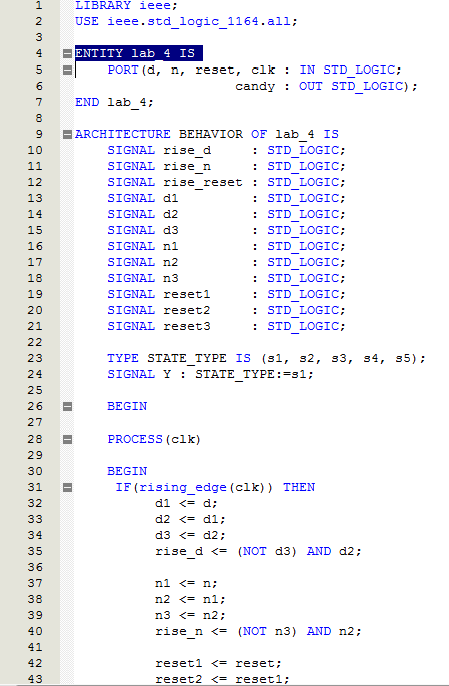
ECPE 174

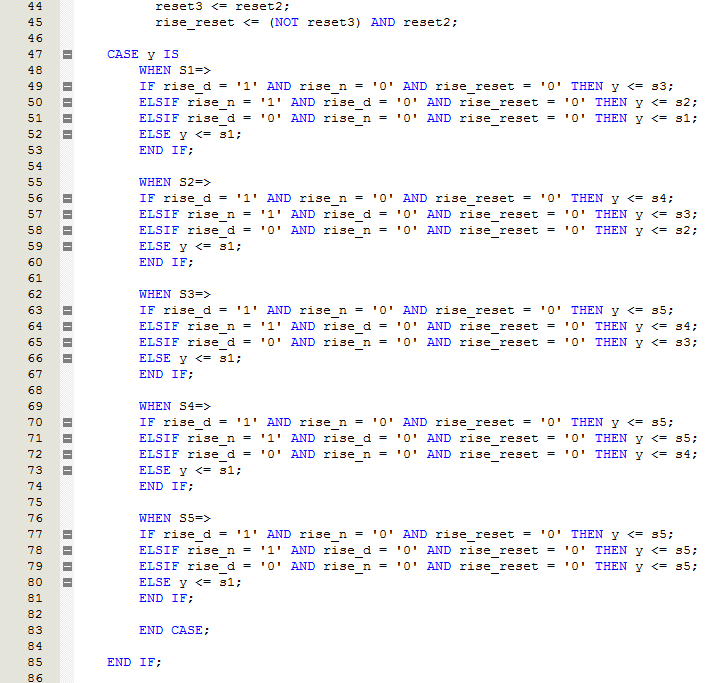
Pre Lab #4

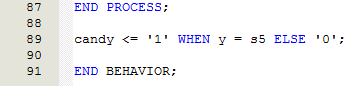
**State Machine Diagram:**

A Moore FSM was used because this seems like the simplest way to design the vending machine.

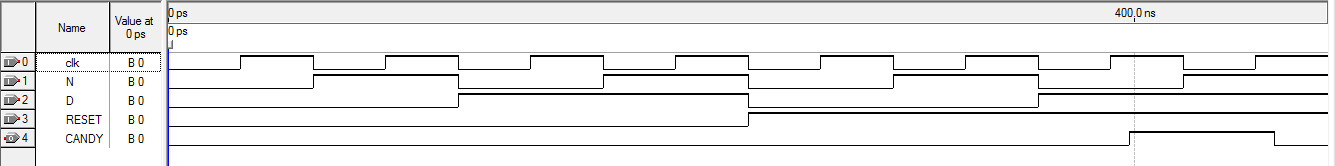
**VHDL:**

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**Waveform Editor:**

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